2024 07 29

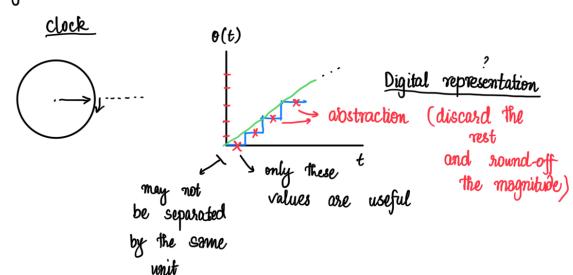
EE1202 - Digital Circuits $HDL \rightarrow Hardware Descriptive Language$

EDA playground

Teams code: yd 14a80

Circuit → closed loop

Digital



Example
Delivery System
Different levels of location

accuracy
Digitizating—Building name/landmark
Acad
H3

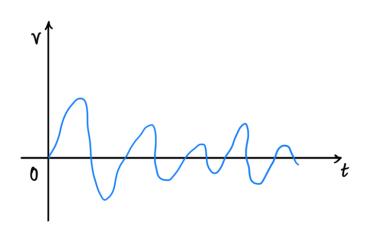
H2
H1

Different lavers of abstraction can be added to simplify

a system.

2024 07 31

information - change in some quantity



communicating V(t)

O fourier, Series taylor expansion

$$V(t) = A_0 + A_1 t + \dots$$

Ao, A1, ... e R

trade-offs:

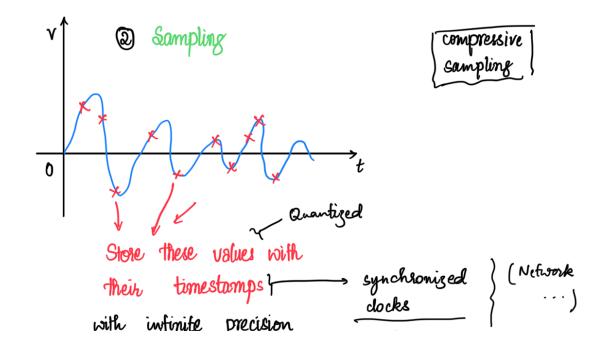
1 no. of terms

(2) decimal accuracy
of coefficients

Quantization error

* We need to limit the number of significant digits to represent a physical quantity in a practical manner.

This can be achieved using quantization.



How to communicate the count of students to another person?

- -> one signal per person (inefficient)
- \rightarrow for every possible count, define a symbol \equiv people know the same (most efficient) language communi rules becomes inefficient.

 \rightarrow 10/16/8/2 symbols

Next: Number Systems

2024/08/01

Number System

→ Decimal → 10 symbols/digits 64 Weight of the digits depend

6×10 position

base index positional number system Radix

- Number systems that are

$$\rightarrow$$
 Binary (Base 2)

radix point (101,001)2

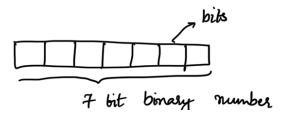
$$= 1 \times 2^{\circ} + 0 \times 2^{1} + 1 \times 2^{2} + 0 \times 2^{-1} + 0 \times 2^{-2} + 1 \times 2^{-3}$$

$$= 1 + 4 + \frac{1}{8} = (5.125)_{10}$$

Binary number system
with two symbols is
most appropriate

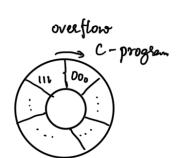
inaccurate
$$\begin{cases} \text{onalog} \\ \text{v} = SR \\ S = \frac{V}{R} \quad \text{divis} \end{cases}$$

Binary Number System



In a n-bit number \longrightarrow total count is 2^n

Decimal to binary



Next: Negative numbers